Non-ideal Models and Simulation of Boost Converters Operating in DCM

Guang-jun Xie, Senior Member, IACSIT, Hai-bin Fang and Xin Cheng

Abstract—Various aspects for the averaged modelling of the Boost converters operating in the discontinuous conduction mode (DCM) are studied. The parasitics of the Boost converter are also considered. A duty ratio constraint that defines the diode conduction interval is identified to be the key for accurate prediction of high-frequency behaviour. A new duty-ratio constraint is proposed that leads to the full-order averaged models of DCM converters. Numerical analyses and experimental measurements confirm that the new models correctly predict the small-signal responses up to the half of the switching frequency and are more accurate than previous models. Moreover, the analytical results are included to show the origin of the high-frequency pole in DCM operation and to explain why the full-order model is capable of accurately predicting it.

Index Terms—Discontinuous Conduction Mode, Boost Converters, Duty Ratio Constraint

I. INTRODUCTION

For the high stability and efficiency of DC-DC switching converter, it has been applied to the fields of mobile communications, industrial equipment, home appliances, and so on. The analyses and design of the switch converter already becomes the key points of the research. However, the pulse-width modulation (PWM) switching converters are strongly non-linear time-varying circuits. It is difficult to accurately find their analytical solutions. So, to find a suitable model for simulation of the DC-DC switching power supply design is an important role.

DC-DC converter has two basic operating modes, namely, continuous conduction mode (CCM) and discontinuous conduction mode. In this paper, the Boost DC-DC converter is taken for example, the average circuit method is used to study its non-ideal model operating in DCM. In the proposed methodology, the so-called duty-ratio constraint is used to make improvement in the model. Thereafter, the calculated transfer-functions for steady-state and dynamic small-signal characteristics are also obtained.

The DCM operation of PWM converters differs from CCM operation by an additional time interval in each switching cycle during which an inductor current is clamped to zero (or a constant when there are multiple energy storage elements). Therefore, the DCM operation typically occurs in the DC-DC converters at light load which means a low load current. Many efforts have been made in the past decades to model DCM converters [1-6], these models fall into two categories, i.e. linear models and non-linear models. Non-linear model can be also called full-order averaged model [7] which shows great improvement over the linear model. Conduction losses based on the switch-cell and parasitics are included in the non-linear model.

II. DUTY-RATIO CONSTRAINT

The inductor current waveform of Boost converter in DCM is shown in Fig. 1.

Fig.1. The inductor current waveform of Boost converter in DCM

A duty-ratio constraint defining the dependency of $d_2$ on other variables is identified to be the key to accurately predict the high-frequency behavior. In the conventional state-space averaging method, a volt-second balance relation of the inductor is used to define the duty-ratio constraint [8]. For the Boost topology, volt-second balance over a switching cycle implies

$$d_2 = \frac{v_i - v_c}{v_c} - d_1$$

In the conventional state-space averaging method, using this expression can derive average state equation of ideal converter.

$$\frac{d\bar{i}_L}{dt} = 0$$

$$\frac{d\bar{v}_C}{dt} = \frac{v_i - v_c}{RC}$$

In equation (2), $\bar{i}_L$ is a linear function, however, in reality...
\( i_L \) is a non-linear function, \( \frac{d\tilde{i}_L}{dt} \) is relevant with the state variables, \( \tilde{i}_L \) and \( \tilde{V}_p \). Equation (1) yields a degenerate model where the dynamics of the inductor current disappear. Since the inductor current is no longer a state variable, this averaged model fails to predict the high-frequency behavior of converters.

In the following, a new definition of \( d_2 \) is derived as
\[
\tilde{i}_L = \frac{i_{\text{max}}}{2} (d_1 + d_2) \tag{4}
\]
And \( i_{\text{max}} = \frac{v_i}{L} d_1 T_s \tag{5} \)

Taking equation (5) into equation (4) can get the solution
\[
d_2 = \frac{2\tilde{i}_L}{d_1 T_s v_i} \tag{6}
\]

Comparing with equation (1), the equation (6) will derive a full order averaged model which can correctly predict the high frequency responses as well as dc and low frequency behavior of DCM converters.

III. NEW NON-IDEAL MODEL FOR BOOST CONVERTER

An essential step in constructing the dynamic averaged-switch model is to identify the three-terminal network of the switched-inductor cell as shown in Fig. 2. Here, the parasitics are decoupled to form an ideal part of the switched-inductor cell (a, p and c) for which a corrected full-order model based on the fast-state dynamics is considered. Then, the parasitics are placed outside the averaged cell and appropriately modified to account for energy dissipation.

Two switching inductance parasitics are equalled to the slip road under the principle of conservation of energy, and then the average model of the switching elements method is used, the switching elements are replaced by a current-controlled current source, the passive switching elements are replaced by a voltage-controlled voltage source, shown in Fig. 3.

\[
\frac{d\tilde{i}_L}{dt} = \frac{1}{L} \left[ \frac{dV_o+(1-d_1-d_2)\tilde{V}_{zp}}{d_1 + d_2} \right]
\]

Based on the above, the small-signal model is given as follow,

\[
\begin{align*}
\hat{v}_i & = \frac{\hat{a}\hat{w}_i}{2L} + \frac{\hat{a}\hat{d}_1}{2L} + b_{1L}\hat{i}_L + b_{1O}\hat{V}_o \\
\hat{i}_L & = \frac{\hat{b}\hat{w}_i}{2L} + \frac{\hat{b}\hat{d}_1}{2L} + b_{1L}\hat{i}_L + b_{1O}\hat{V}_o
\end{align*}
\]

(1) The input-to-output transfer function is
\[ G_{v}(s) = \frac{\hat{v}_{v}(s)}{\hat{v}_{i}(s)} = \frac{1 - b_{1} - a_{1}(sL + R_{E} + b_{3})}{1 + b_{4} + (sL + R_{E} + b_{3})(sR_{C} + sR_{C} + 1)} \]

\[ = \frac{k_{s} + t_{1}}{t_{0} + (k_{2s} + t_{2})(k_{3s} + t_{3})} \]

\[ = \frac{k_{2s}s^{2} + k_{2s}s + k_{2s}s + k_{2s}s + k_{2s}s + t_{3} + t_{4}}{t_{2} + t_{4} + 1 + s/2s2 + 1 + s/2s2 + 1 + s/2s2 + 1 + s/2s2} \]

\[ = \frac{1 + s/2s2}{1 + s/2s2} \]  

(11)

(2) The control-to-output transfer function is

\[ G_{w}(s) = \frac{\hat{v}_{w}(s)}{\hat{d}(s)} = \frac{t_{4} + t_{5}}{t_{2} + t_{4}} \]

\[ = \frac{1 + s/2s2}{1 + s/2s2} \]  

(12)

The control-to-output transfer function includes the right-half plane (RHP) zero \( w_{2,3} \), in low-frequency it can be neglected, but in high-frequency it makes the phase of \( G_{w}(s) \) increased -90°. As the Fig. 6(b) shows, the new averaged model gives the most accurate response compared to detailed simulation.

(3) The converter input impedance \( Z(s) \) is

\[ Z(s) = \frac{\hat{v}_{v}(s)}{\hat{i}(s)} \]

\[ = \frac{t_{2} + t_{4}}{t_{2} + t_{4}} \]  

(13)

(4) The converter output impedance \( Z_{out}(s) \) is

\[ Z_{out}(s) = \frac{\hat{v}_{o}(s)}{\hat{i}_{out}(s)} \]

\[ = \frac{t_{2} + t_{4}}{t_{2} + t_{4}} \]  

(14)

IV. SIMULATION AND ANALYSIS

In this paper, the Matlab/Simulink is chosen as the simulation tool. The parameters are, \( V_{i} = 5V \), \( V_{o} = 10V \), \( I_{o} = 0.2A \), \( R = 50\Omega \), \( L = 19.2\mu H \), \( R_{L} = 3.6m\Omega \), \( C = 1000\mu F \), \( R_{C} = 2m\Omega \), \( R_{on} = 10m\Omega \), \( R_{f} = 1m\Omega \), \( V_{F} = 0.4V \), \( f_{i} = 50kHz \).

Taking \( D_{2} = \frac{2LL_{i}}{L_{2}T_{V}} - D_{1} \), \( L_{i} = \frac{I_{o}}{D_{2}} \) (11)-(14), specific values can be calculated. The magnitude and phase of the corresponding transfer-functions are plotted in Fig. 6, in which, I, II, III correspond to the following conditions respectively. I. \( R_{E} \neq 0 \), \( R_{C} \neq 0 \), \( V_{F} \neq 0 \), \( d_{2} = \frac{V_{i}}{V_{o} - V_{i}} \cdot d_{1} \), Taking into account the parasitics, but the duty-ratio constraint is not considered. II. \( R_{E} \neq 0 \), \( R_{C} \neq 0 \), \( V_{F} \neq 0 \), \( d_{2} = \frac{V_{i}}{V_{o} - V_{i}} \cdot d_{1} \), for the ideal model.

Fig. 5. The model of Boost converter in Matlab/Power System Blockset

To get accurate and repeatable measurement points, In Fig. 5, the hardware prototype is built in Matlab/Power System Blockset, using the FFT utility to extract the necessary magnitude and phase information.
leads to circuit averaged models that are different from previous ones. Numerical simulation and experimental measurement of Boost converter confirm that the new model correctly predicts the small-signal response.

V. CONCLUSION

In this paper, a general approach has been presented to generate the circuit averaged models of DC-DC converters operating in DCM. The proposed method relies on a duty-ratio constraint and the parasitics of circuit elements are considered. It accurately captures the high-frequency converter dynamics. The proposed model has been verified with a hardware prototype and has shown an improvement over several existing models.

REFERENCES


Guang-jun Xie received the B.S. degree and the M.S. degree from Hefei University of Technology, in 1992 and 1995, respectively. He received the Ph.D. degree from University of Science and Technology of China in 2002. He is now a Professor of Hefei University of Technology, and his research interests include IC design, computational intelligence.